

Certificate

Certified that the thesis entitled “**Synthesis of Analog IC Building Blocks**” being submitted by **Ms. Alpana Kansal, Registration No. 9010451** to the **Department of Electronics and Communication Engineering, Thapar University, Patiala, India** in fulfillment of the requirements for the award of degree of “**Doctor of Philosophy**” is a record of bonafide research work carried out by her under my guidance and supervision during the period 2002-2009. She has fulfilled the requirements for the submission of this thesis, which has reached the requisite standard. The matter presented in this thesis does not incorporate any material previously published or written by any other person except where due reference is made in the text.

The results contained in this thesis have not been submitted either in parts or in full to this or any other Institute/University for the award of any degree or diploma.

Date: November 7, 2010



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