



JAYPEE UNIVERSITY OF ENGINEERING & TECHNOLOGY

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DECLARATION BY THE SCHOLAR

I hereby declare that the research work reported in the Ph.D thesis entitled **“Parallel VLSI Architecture for Lifting 2D-DWT using RADIX-8 Booth Multiplier”**, submitted at **Jaypee University of Engineering and Technology, Guna**, is an authentic record of my research work carried out under the supervision of **Prof. Basant Kumar Mohanty**. I have not submitted this research work elsewhere for any other degree or diploma.

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