

LIST OF PUBLICATIONS

1. Mohanty, B.K. and Choubey, A. (2017), “Efficient Design for radix-8 Booth Multiplier & its Application in Lifting 2-D DWT,” *Circuits, Systems, and Signal Processing, Springer*, Vol. 36, No. 3, pp. 1129-1149.(SCI)
2. Choubey, A and Mohanty, B.K. (2017), “A Block Based Area- Delay Efficient Architecture for Multi-level Lifting 2-D DWT,” *International Journal of Computer Applications*” vol. 169, No.4.
3. Choubey, A. and Mohanty, B.K. (2017), “Novel Data Access Scheme & Efficient Parallel Architecture for Multi-level 2-D DWT,” *Circuits, Systems, and Signal Processing, Springer*. (Under review) (SCI)